

FIG. 12
CONVENTIONAL ART



REPLACEMENT SHEET

01R00073

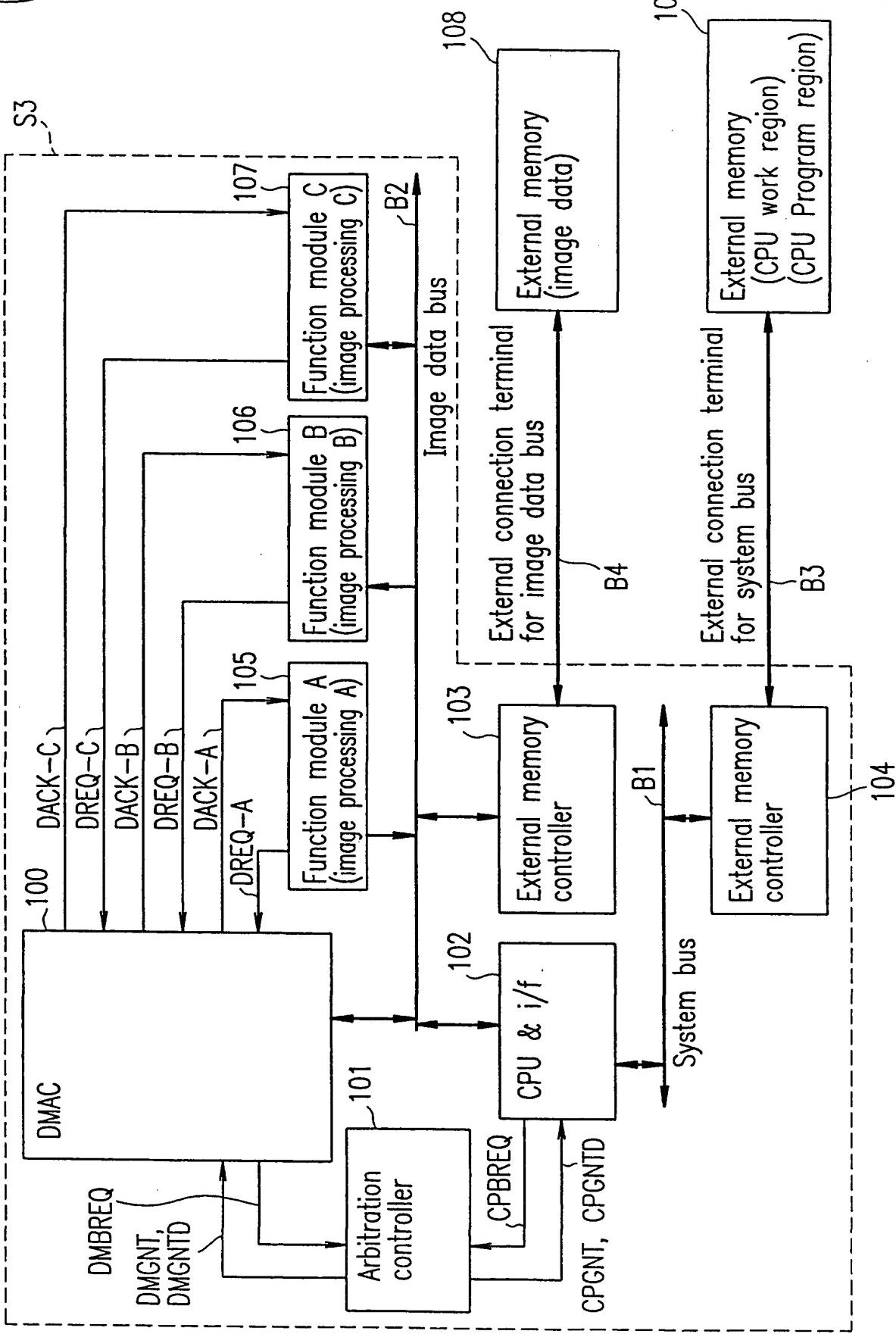
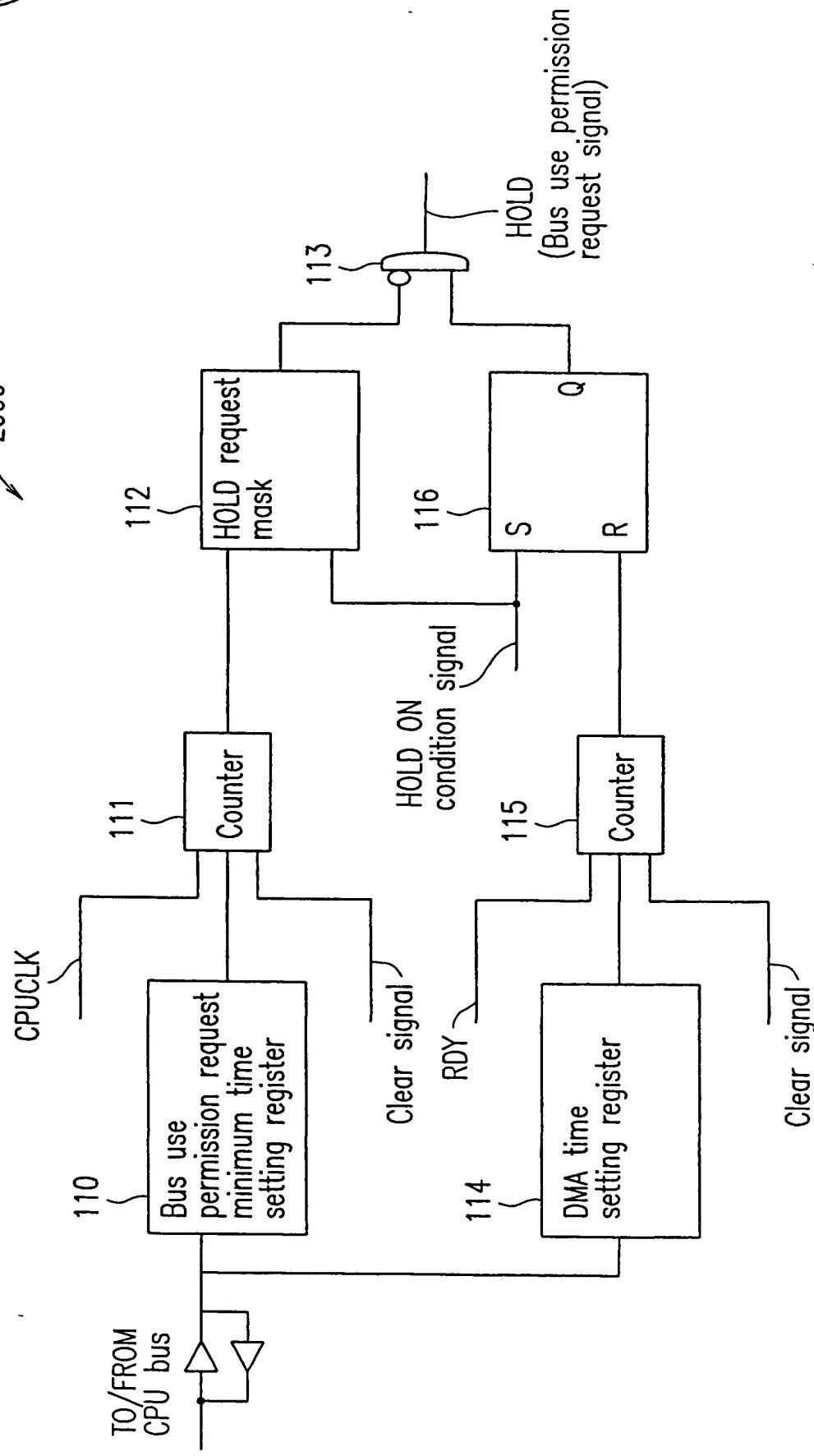


FIG. 13
CONVENTIONAL ART



REPLACEMENT SHEET

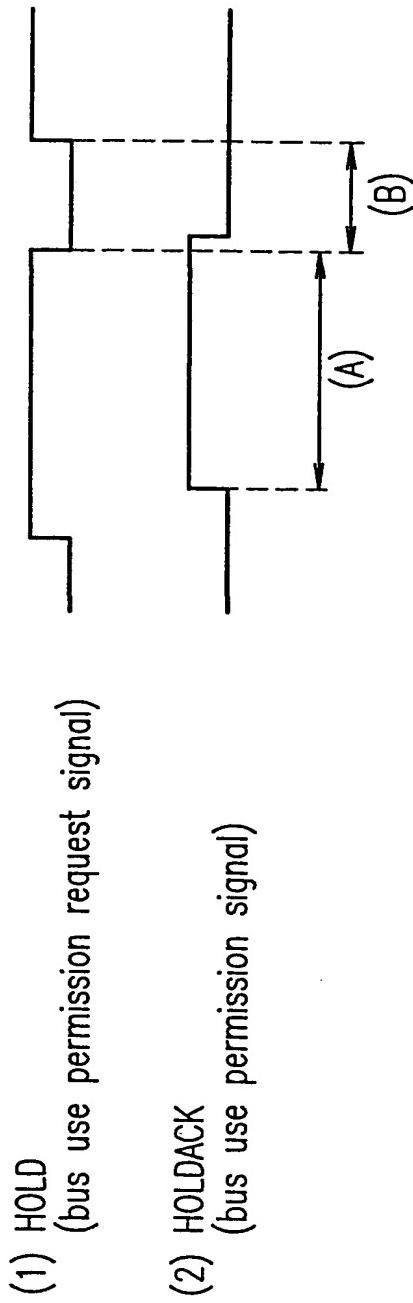
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FIG. 14
CONVENTIONAL ART



REPLACEMENT SHEET

01R00073



(A) DMA transfer time
(CPU or DMAC which has bus use permission uses bus for data transfer)

(B) Bus use permission request minimum time
(time consumed until a bus use permission can be requested after the data transfer during time (A) is completed)